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1 A LOTOS extension for the performance analysis of distributed systems 90%



Marco Ajmone Marsan , Andrea Bianco , Luigi Ciminiera , Riccardo Sisto , Adriano Valenzano

IEEE/ACM Transactions on Networking (TON) April 1994
Volume 2 Issue 2

2 Effectively prioritizing tests in development environment 84%



Amitabh Srivastava , Jay Thiagarajan

ACM SIGSOFT Software Engineering Notes , Proceedings of the international symposium on Software testing and analysis July 2002
Volume 27 Issue 4

Software testing helps ensure not only that the software under development has been implemented correctly, but also that further development does not break it. If developers introduce new defects into the software, these should be detected as early and inexpensively as possible in the development cycle. To help optimize which tests are run at what points in the design cycle, we have built *Echelon*, a test prioritization system, which prioritizes the application's given set of tests, based ...

3 Papers: A novel wide-band audio transmission scheme over the Internet 84%



with a smooth quality degradation

Fulvio Babich , Marko Vitez

ACM SIGCOMM Computer Communication Review January 2000
Volume 30 Issue 1

Real-time delivery of multimedia information over the Internet is finding increasing interest. This paper considers wide-band audio transmission utilizing a priority scheme. The proposed scheme complies with both the new Internet Protocol Version 6 (IPv6) and the current Internet Protocol Version 4 (IPv4), providing that, in the latter case, routers are set to manage priority. A new queuing algorithm, namely

Priority Weighted Fair Queuing (PWFQ), is defined and evaluated. A scalable audio encode ...

- 4** Performance evaluation of routing algorithms under various network configuration parameters 84%



Shin-Jer Yang

International Journal of Network Management July 1997

Volume 7 Issue 4

Routing is a relevant issue for maintaining good performance and successfully operating in a network. Many types of routing algorithms have been proposed, such as shortest-path, centralized, distributed and flow-based. This article not only defines the relationship among performance indicators and network configuration parameters, but also presents a practical method for improving routing decisions. © 1997 John Wiley & Sons, Ltd.

- 5** SAIRVO: a temporal planning system 82%



M. Lizotte , B. Moulin

Proceedings of the second international conference on Industrial and engineering applications of artificial intelligence and expert systems - Volume 1

June 1989

Our approach of planification which derives from both expert systems and planning systems design techniques (MOU 86)(MOU 88), is based on several principles dealing with time management and the planner's universe modelling, the representation of its motivations and behavioural knowledge, the dynamic construction of its planning knowledge, as well as the use of metaplanning techniques to coordinate the planners reasoning activities. We will present a brief outline of research works related t ...

- 6** Virtual 3D camera composition from frame constraints 82%



William Bares , Scott McDermott , Christina Boudreaux , Somying Thainimit

Proceedings of the eighth ACM international conference on Multimedia October 2000

We have designed a graphical interface that enables 3D visual artists or developers of interactive 3D virtual environments to efficiently define sophisticated camera compositions by creating storyboard frames, indicating how a desired shot should appear. These storyboard frames are then automatically encoded into an extensive set of virtual camera constraints that capture the key visual composition elements of the storyboard frame. Visual composition elements include the size and position of ...

- 7** Chip layout optimization using critical path weighting 80%



A. E. Dunlop , V. D. Agrawal , D. N. Deutsch , M. F. Jukl , P. Kozak , M. Wiesel

21st Proceedings of the Design Automation Conference on Design automation June 1984







A chip layout procedure for optimizing the performance of critical timing paths in a synchronous digital circuit is presented. The procedure uses the path analysis data produced by a static timing analysis program to generate weights for critical nets on clock and data paths. These weights are then used to bias automatic placement and routing in the layout program. This approach is shown to bring the performance of the chip significantly closer to that of an ideal layout which is assumed to ...

- 8** Chip layout optimization using critical path weighting 80%



A. E. Dunlop , V. D. Agrawal , D. N. Deutsch , M. F. Jukl , P. Kazak

Papers on Twenty-five years of electronic design automation June 1988

- 9** Object model resurrection ---- an object oriented maintenance activity 80%
 Gokul V. Subramaniam
Proceedings of the 22nd international conference on Software engineering June 2000
 This paper addresses the problem of reengineering object-oriented systems that have incurred increased maintenance cost due to long development time-span and project lifecycle. When an Incremental Approach is used to develop an object-oriented system, there is a risk that the class design and the overall object model will deteriorate in quality with each increment. A recent research work suggested a process activity (Class Deterioration Detection and Resurrection - CDDR pro ...
- 10** Quality function deployment usage in software development 80%
 Stephen Haag , M. K. Raja , L. L. Schkade
Communications of the ACM January 1996
 Volume 39 Issue 1
- 11** A new min-cut placement algorithm for timing assurance layout design 80%
 meeting net length constraint
 Masayuki Terai , Kazuhiro Takahashi , Koji Sato
Conference proceedings on 27th ACM/IEEE design automation conference January 1991
 This paper presents a new min-cut placement algorithm for timing assurance layout design. When critical nets are given net length constraints, the proposed algorithm can place cells so that the constraints may be met. This algorithm is built into the layout system for gate arrays called GALOP [1] and has been successfully applied to clock skew control of an ECL 12K-gate gate array.
- 12** Knowledge-based approaches to government benefits analysis 77%
 Marc Lauritsen
Proceedings of the third international conference on Artificial intelligence and law May 1991
- 13** Tools: Virtual routers: a tool for networking research and education 77%
 Florian Baumgartner , Torsten Braun , Eveline Kurt , Attila Weyland
ACM SIGCOMM Computer Communication Review July 2003
 Volume 33 Issue 3
 Virtual routers are software entities, i.e. user space processes, emulating IP routers on one or several (Linux) computers. Virtual routers can be used for both networking research and education. In contrast to simulation, virtual routers process packets in real-time and the virtual router code is similar to code in real routers. In the case of research, larger network test-beds can be built using a relatively small number of computers. New functionalities such as new queuing mechanisms are supp ...
- 14** Planning in MAS: Continual coordination through shared activities 77%
 Bradley J. Clement , Anthony C. Barrett
Proceedings of the second international joint conference on Autonomous agents and multiagent systems July 2003
 Interacting agents that interleave planning and execution must reach consensus on their commitments to each other. In domains where agents have varying degrees of

interaction and different constraints on communication and computation, agents will require different coordination protocols in order to efficiently reach consensus in real time. We briefly describe a largely unexplored class of real-time, distributed planning problems (inspired by interacting spacecraft missions), new challenges they ...

15 Prediction of interconnect delay in logic synthesis

77%



H. H. -F. Jyu , S. Malik

Proceedings of the 1995 European conference on Design and Test March 1995

IC designers are now increasingly concerned about the delay due to interconnection wires. In the past, these effects have been largely ignored during logic design- primarily due to their negligible contributions and also because of the difficulty of predicting the wiring resulting from the subsequent layout stage. In this paper, an estimation model is proposed to predict the average wire length for each net in a given gate-level netlist and a particular layout tool.

16 Self-assessment procedure XX

77%



J. Rosenberg , A. L. Ananda , B. Srinivasan

Communications of the ACM February 1990

Volume 33 Issue 2

A self-assessment procedure on operating systems

17 Survey of design goals for operating systems

77%



David H. Abernathy , John S. Mancino , Charls R. Pearson , Dona C. Swiger

ACM SIGOPS Operating Systems Review January 1974

Volume 8 Issue 1

The paper reports the results of a literature search on the subject "design goals for operating systems." In addition, design goals of specific operating systems are reviewed and a general set of operating system design goals is developed. Special-purpose design goals and conflicts among design goals are also discussed.

18 Prioritized resource allocation for stressed networks

77%



Cory C. Beard , Victor S. Frost

IEEE/ACM Transactions on Networking (TON) October 2001

Volume 9 Issue 5

Overloads that occur during times of network stress result in blocked access to all users, independent of importance. These overloads can occur because of degraded resource availability or abnormally high demand. Public broadband networks must dynamically recognize some multimedia connections as having greater importance than others and allocate resources accordingly. A new approach to connection admission control is proposed that uses an upper limit policy to optimize the admission of connectio ...

19 A quick safari through the reconfiguration jungle

77%



Patrick Schaumont , Ingrid Verbauwhede , Kurt Keutzer , Majid Sarrafzadeh

Proceedings of the 38th conference on Design automation June 2001

Cost effective systems use specialization to optimize factors such as power consumption, processing throughput, flexibility or combinations thereof. Reconfigurable systems obtain this specialization at run-time. System reconfigurable has a vertical, a horizontal and a time dimension. We organize this design space as the reconfiguration hierarchy, and discuss the design methods that deal with it. Finally, we survey existing commercial platforms that support reconfiguration and situate them i ...

20 Generating virtual camera compositions

77%



William Bares , Byungwoo Kim

Proceedings of the 6th international conference on Intelligent user interfaces

January 2001

This paper describes work in progress to automatically generate camera shots featuring the composition techniques of expert photographers. This effort builds upon an automated camera planner that computes a shot satisfying a given set of constraints. In this prior work, users manually specify the set of constraints and the numeric parameters for each. A typical two subject shot can be described by eleven constraints involving sixty numeric parameters. This work aims to develop a high-level ...

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